

LAP-A Specification

Model		LAP-16128U	LAP-32128U-A	LAP-321000U-A
Operating System		Windows	Windows	Windows
		98SE/ME/2000/XP	98SE/ME/2000/XP	98SE/ME/2000/XP
Interface		USB2.0	USB2.0	USB2.0
Power	Power	USB	USB	USB
	Power Dissipation (running)	1W	1W	1W
	Power Dissipation (startup)	2W	2W	2W
Sample rate	Internal clock (Timing Mode)	100HZ~200MHZ	100HZ~200MHZ	100HZ~200MHZ
	External Clock (State Mode)	100MHZ	100MHZ	100MHZ
	Bandwidth	75MHZ	75MHZ	75MHZ
Memory	Memory	4Mbits	4Mbits	32Mbits
	Depth (per channel)	128Kbits (Max 32Mbits for Compression)	128Kbits (Max 32Mbits for Compression)	1Mbits (Max 255Mbits for Compression)
Trigger	Condition	Pattern/Edge	Pattern/Edge	Pattern/Edge
	Trigger channel	16CH	32CH	32CH
	Pre/Post Trigger	YES	YES	YES
	Waveform Trigger Width	NO	NO	NO
	Trigger Level	One Level	One Level	One Level
	Trigger Count	1~65535	1~65535	1~65535
Threshold Voltages	Working range	-6V ~ +6V	-6V ~ +6V	-6V ~ +6V
	Accuracy	±93mV	±93mV	±93mV
Maximum Input Voltage		±30V	±30V	±30V
Impedance		500KΩ/10pF	500KΩ/10pF	500KΩ/10pF
Bus / Protocol	I ² C / UART	Free	Free	Free
	SPI/ Manchester/ SSI/ Microwire	Option	Free	Free
	SIGNIA RF MODULE / I ² S	Option	Option	Option
	PS/2 / USB1.1	Option	Option	Option
	1-WIRE / HDQ / CAN BUS	Option	Option	Free
Data Skew		<1.5ns	<1.5ns	<1.5ns
Package Contents				
Analyzer (130mm x 100mm x 30mm / 175g)		1	1	1
USB cable		1	1	1
16/32 channel testing cable package (25cm)		1 Pack 8pin*2 / 2pins*1/ 1pin*1	1 Pack 16pins*1 / 8pin*2 / 2pins*1/ 1pin*1	1 Pack 16pins*1 / 8pin*2 / 2pins*1/ 1pin*1
Probes (test grabbers)		1 Pack 20 pieces/Package	1 Pack 36 pieces/Package	1 Pack 36 pieces/Package
Software / Documentation CD		1	1	1
Installation Guide		1	1	1



Recommended Tool Bag



Features

- SPI, I²C, UART, 1-WIRE, HDQ, CAN BUS, USB1.1, SIGNIA RF MODULE, I²S, PS/2, Microwire, SSI, Manchester ...bus communication protocol analysis allows you to easily develop circuits interfacing as the protocols mentioned.
- Supports time stamps, bus values, binary, hex, ASCII, decimal, etc display with timing wave.
- **Memory per Channel**
 - 128KB (or 1MB*) per channel without compression.
 - Hardware compression ratio up to 255:1 depending on activity.
 - Analyze more data at one time and simplify your debugging task.
- **Flexible Sampling Frequency for accuracy**
 - Synchronous operation (use test-board clock) : Max 100MHz
 - Asynchronous operation (use logic analyser clock) : 100Hz ~ 200MHz
- **Hook up with benchtop tools e.g. Oscilloscope, etc.**

Equipped with output pins to synchronize logic analyzer operations with other benchtop analysis tools.

 - Read Out pin
 - Trigger Out pin
 - Start Out pin



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