



Supported Devices

ALTERA
Atmel
Lattice
XILINX

Standard Accessories

Main unit
Printer cable x 1
ISP Cable
DC 12V/500mA adaptor
Operation manual

Physical & Environmental Specification

Dimension:
15.0cm X 8.0cm X 3.0cm
Weight: 200gs
Temperature: +5°C~45°C
Humidity: up to 90%
non- condensing
Operation Voltage:
110V AC or 220V AC
Frequency Range:
50~60 Hz
Power Consumption:
6 W maximum

PLD (Programming Logic Device) is most common and easy use logic device. It has high performance, lower cost flexible design and easy field configuration or customization advantages. PLD has a lot of suppliers and they all have similar operating system, but they all have different "download kit" to support. And that is the reason why we develop the universal IN SYSTEM PROGRAMMER(LP-UDT1)and customer can use LP-UDT1 to program different brands CPLD / FPGA devices.

Characters

1. Support JTAG and ISP specification, on board program function for CPLD and FPGA devices.
2. Use vendor's developing system to process design, compile, simulate, debug, on board program and data download.
3. Support FPGA and CPLD manufacture's JTAG /ISP function, such as ALTERA, Atmel Lattice, XILINX... etc.

4. Excellent circuit structure, best programming quality, lower yield rate and protection designed to give the best service to your expensive device.
5. Use Printer port to connect with PC & Notebook, easy operating.
6. Small, portable and fine structure.
7. Support low voltage to 1.8V device.

Hardware Features

1. 6 standard JTAG and ISP cables
2. Operating voltage: +1.8V~6.0V
3. Output programming voltage: VHH + 12.0 V 200mA
4. Output/Input: Parallel port
5. Selective switch for different vendors
6. Red power indicate light

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